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Parson 3-2-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): D.E. Parson et al.

Case: 3-2-1-4

Serial No.: 09/583,057

Filing Date: May 30, 2000

Group: 2184

Examiner: Timothy M. Bonura

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Laura M. Hanmer Date: June 13, 2003

Title: Control Method and Apparatus for Testing of Multiple Processor Integrated Circuits and Other Digital Systems

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following remarks are submitted in response to the outstanding non-final Office Action dated March 13, 2003 in the above-identified application.

REMARKS

The present application was filed on May 30, 2000 with claims 1-20. Claims 1-20 remain pending. Claims 1, 13, 15, 19 and 20 are the independent claims.

Applicants respectfully request reconsideration of the present application in view of the remarks below.

Applicants initially traverse the rejections set forth in the Office Action on the ground that the rejections fail to provide a clear disposition for each of the pending claims of the application. For example, with regard to dependent claims 6 and 7, the Office Action at page 8, paragraphs 22 through 24, indicates that these claims contain allowable subject matter. However, the Office Action

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